

PRODUCT/PROCESS CHANGE NOTIFICATION (PCN)

PCN #: A082100-1 Date: 08/21/2000 Product Affected: PCI9052 Manufacturing Location Affected: Taiwan Date Effective: 08/01/00	MEANS OF DISTINGUISHING CHANGED DEVICES <input type="checkbox"/> Product Mark <input type="checkbox"/> Back Mark <input type="checkbox"/> Date Code <input type="checkbox"/> Other						
<table style="width: 100%;"> <tr> <td style="width: 50%;"> Contact: Ted Rudolph Title: Quality Engineering Manager Phone: 408-328-3530 Fax #: 408-732-5916 E-mail: qa@plxtech.com </td> <td style="width: 50%;"> Additional Data: N/A Samples: N/A </td> </tr> </table>		Contact: Ted Rudolph Title: Quality Engineering Manager Phone: 408-328-3530 Fax #: 408-732-5916 E-mail: qa@plxtech.com	Additional Data: N/A Samples: N/A				
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DESCRIPTION AND PURPOSE OF CHANGE: <table style="width: 100%;"> <tr> <td style="width: 35%;"> <input type="checkbox"/> Die Technology <input type="checkbox"/> Wafer Fabrication process <input type="checkbox"/> Assembly Process <input type="checkbox"/> Equipment <input type="checkbox"/> Material <input type="checkbox"/> Testing <input type="checkbox"/> Manufacturing Site <input type="checkbox"/> Data Sheet </td> <td style="width: 65%;"> Due to capacity constraints, PLX has qualified TSMC's Fab 9. This additional fab capacity will give PLX more flexibility in the manufacture of its PCI9052 to meet the increasing demands from our customers. The current fab (F2) is a 6" wafer fab, and the additional fab (F9) is a 8" wafer fab. Consequently, in the future, the PCI9052 may be manufactured in either fab. Part marking will identify the fab origin. </td> </tr> </table>		<input type="checkbox"/> Die Technology <input type="checkbox"/> Wafer Fabrication process <input type="checkbox"/> Assembly Process <input type="checkbox"/> Equipment <input type="checkbox"/> Material <input type="checkbox"/> Testing <input type="checkbox"/> Manufacturing Site <input type="checkbox"/> Data Sheet	Due to capacity constraints, PLX has qualified TSMC's Fab 9. This additional fab capacity will give PLX more flexibility in the manufacture of its PCI9052 to meet the increasing demands from our customers. The current fab (F2) is a 6" wafer fab, and the additional fab (F9) is a 8" wafer fab. Consequently, in the future, the PCI9052 may be manufactured in either fab. Part marking will identify the fab origin.				
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RELIABILITY/QUALIFICATION SUMMARY: Reliability data will be available 10-31-2000							
CUSTOMER ACKNOWLEDGEMENT OF RECEIPT: Please use the acknowledgement below or E-mail qa@plxtech.com to grant approval or request additional information. If PLX does not receive acknowledgement within 30 days of this notice it will be assumed that this notice is acceptable.							
<table style="width: 100%;"> <tr> <td style="width: 45%;">Customer: _____</td> <td style="width: 55%;"><i>Y Approval for shipments prior to effective date.</i></td> </tr> <tr> <td>Name/Date: _____</td> <td>E-Mail Address: _____</td> </tr> <tr> <td>Title: _____</td> <td>Phone# /Fax# : _____</td> </tr> </table> <p>Customer Comments:</p> <p>_____</p> <p>_____</p> <p>_____</p> <p>REC'D. BY: _____ DATE: _____</p>		Customer: _____	<i>Y Approval for shipments prior to effective date.</i>	Name/Date: _____	E-Mail Address: _____	Title: _____	Phone# /Fax# : _____
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