

# HCPL-0700, HCPL-0701, HCNW138, HCNW139, 6N139, 6N138

## Low Input Current, High Gain Optocouplers

### Description

These Broadcom<sup>®</sup> high gain series couplers use a Light Emitting Diode and an integrated high gain photodetector to provide extremely high current transfer ratio between input and output. Separate pins for the photodiode and output stage result in TTL-compatible saturation voltages and high speed operation. Where desired the  $V_{CC}$  and  $V_O$  terminals may be tied together to achieve conventional photodarlington operation. A base access terminal allows a gain bandwidth adjustment to be made.

The 6N139, HCPL-0701, and HCNW139 are for use in CMOS, LSTTL or other low power applications. A 400% minimum current transfer ratio is guaranteed over 0 to 70°C operating range for only 0.5 mA of LED current.

The 6N138, HCPL-0700, and HCNW138 are designed for use mainly in TTL applications. Current Transfer Ratio (CTR) is 300% minimum over 0 to 70°C for an LED current of 1.6 mA (1 TTL Unit load). A 300% minimum CTR enables operation with 1 TTL Load using a 2.2 k $\Omega$  pull-up resistor.

### Features

- High current transfer ratio: 2000% typical (4500% typical for HCNW139/138)
- Low input current requirements: 0.5 mA

- TTL compatible output: 0.1V  $V_{OL}$  typical
- Performance guaranteed over temperature 0°C to 70°C
- Base access allows gain bandwidth adjustment
- High output current: 60 mA
- Safety approval:
  - UL recognized: 3750V<sub>rms</sub> for 1 minute and 5000V<sub>rms</sub><sup>1</sup> for 1 minute per UL 1577
  - CSA approved
  - IEC/EN/DIN EN 60747-5-5 approved with  $V_{IORM} = 1414V_{peak}$  for HCNW139 and HCNW138
- Available in 8-Pin DIP or SOIC-8 footprint or widebody package
- MIL-PRF-38534 hermetic version available (HCPL-5700/1)

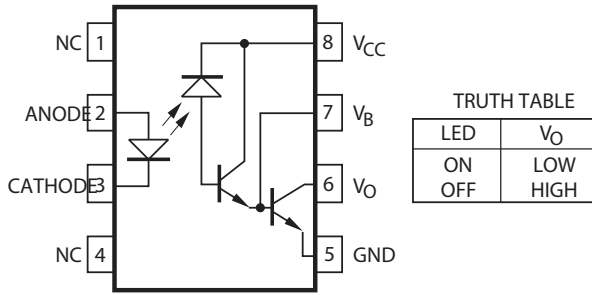
### Applications

- Ground isolate most logic families: TTL/TTL, CMOS/TTL, CMOS/CMOS, LSTTL/TTL, CMOS/LSTTL
- Low input current line receiver
- High voltage insulation (HCNW139/138)
- EIA RS-232C line receiver
- Telephone ring detector
- 117V<sub>ac</sub> line voltage status indicator: low input power dissipation
- Low power systems: ground isolation

**CAUTION!** Take normal static precautions in handling and assembly of this component to prevent damage, degradation, or both that may be induced by ESD. The components featured in this datasheet are not to be used in military or aerospace applications or environments.

1. 5000V<sub>rms</sub>/1 minute rating is for HCNW139/138 and Option 020 (6N139/138) products only. A 0.1- $\mu$ F bypass capacitor connected between pins 8 and 5 is recommended.

# Functional Diagram



# Selection Guide

Selection for lower input current down to 250  $\mu$ A is available upon request.

The HCPL-0701 and HCPL-0700 are surface mount devices packaged in an industry-standard SOIC-8 footprint.

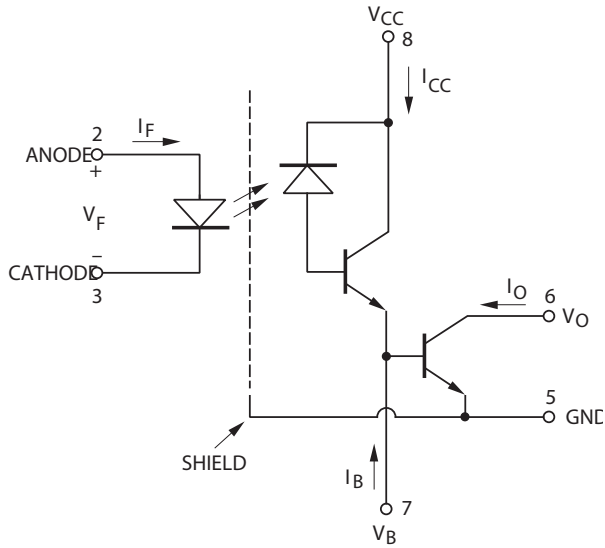
The SOIC-8 does not require “through holes” in a PCB. This package occupies approximately one-third the footprint area of the standard dual-in-line package. The lead profile is designed to be compatible with standard surface-mount processes.

The HCNW139 and HCNW138 are packaged in a widebody encapsulation that provides creepage and clearance dimensions suitable for safety approval by regulatory agencies worldwide.

8-Pin DIP (300 mil)		Small Outline SO-8		Widebody (400 mil)	Minimum Input ON Current (I <sub>F</sub> )	Minimum CTR	Absolute Maximum V <sub>CC</sub>	Hermetic
Single Channel Package	Dual Channel Package	Single Channel Package	Dual Channel Package	Single Channel Package				Single and Dual Channel Packages
	HCPL-	HCPL-	HCPL-					HCPL-
6N139	2731 <sup>a</sup>	0701	0731	HCNW139	0.5 mA	400%	18V	—
6N138	2730 <sup>a</sup>	0700	0730	HCNW138	1.6 mA	300%	7V	—
HCPL-4701 <sup>a</sup>	4731 <sup>a</sup>	070A <sup>a</sup>	073A <sup>a</sup>	—	40 $\mu$ A	800%	18V	—
					0.5 mA	300%	20V	5701 <sup>a</sup> 5700 <sup>a</sup> 5731 <sup>a</sup> 5730 <sup>a</sup>

a. Technical data are on separate Broadcom publications.

# Schematic



# Ordering Information

6N138, 6N139, HCPL-0700 and HCPL-0701 are UL Recognized with 3750V<sub>rms</sub> for 1 minute per UL1577 and are approved under CSA Component Acceptance Notice #5, File CA 88324.

Part Number	Option		Package	Surface Mount	Gull Wing	Tape and Reel	UL 5000 Vrms/ 1 Minute Rating	IEC/EN/DIN EN 60747-5-5	Quantity
	RoHS Compliant	Non-RoHS Compliant							
6N138	-000E	no option	300 mil DIP-8						50 per tube
6N139	-300E	#300		X	X				50 per tube
	-500E	#500		X	X	X			1000 per reel
	-020E	#020					X		50 per tube
	-320E	#320		X	X		X		50 per tube
	-520E	#520		X	X	X	X		1000 per reel
	-060E	#060						X	50 per tube
	-360E	#360		X	X			X	50 per tube
	-560E	#560	X	X	X		X	1000 per reel	
HCPL-0700	-000E	no option	SO-8	X					100 per tube
HCPL-0701	-500E	#500		X		X			1500 per reel
	-060E	#060		X				X	100 per tube
	-560E	#560		X		X			1500 per reel
HCNW138	-000E	no option	400 mil Widebody DIP-8				X	X	42 per tube
HCNW139	-300E	#300		X	X		X	X	42 per tube
	-500E	#500		X	X	X	X	X	750 per reel

To order, choose a part number from the part number column and combine with the desired option from the option column to form an order entry.

**Example 1:**

6N138-560E to order product of 300 mil DIP Gull Wing Surface Mount package in Tape and Reel packaging with IEC/EN/DIN EN 60747-5-5 Safety Approval and RoHS compliant.

**Example 2:**

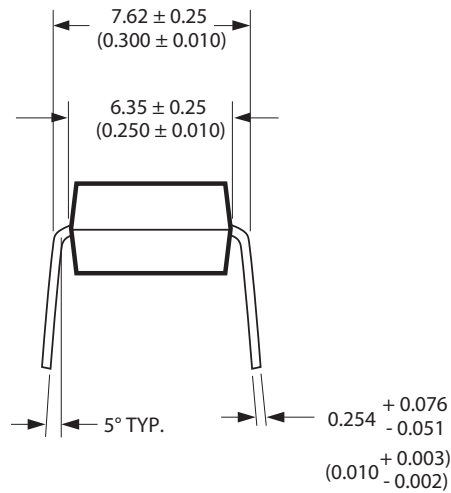
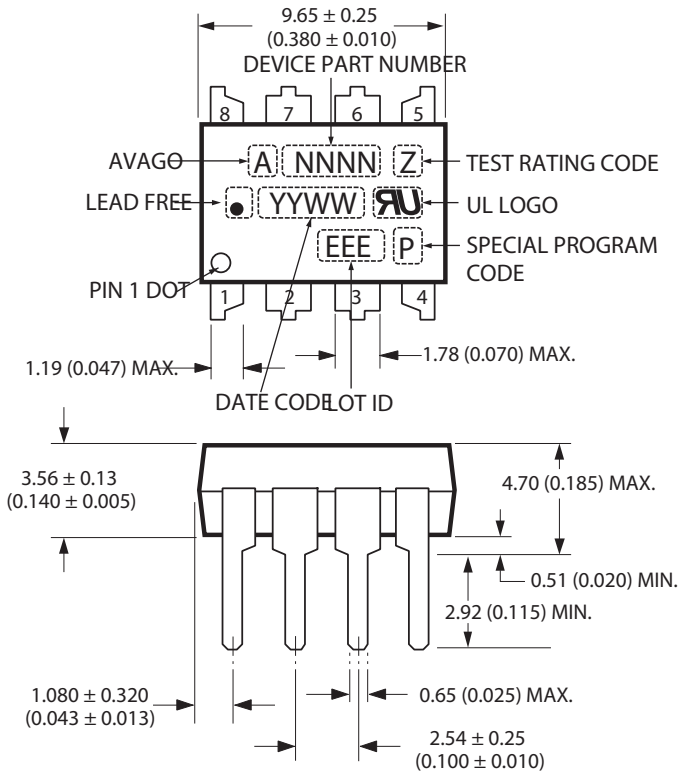
HCPL-0700 to order product of 300 mil DIP package in Tube packaging and non-RoHS compliant.

Option data sheets are available. Contact your Broadcom sales representative or authorized distributor for information.

Remarks: The notation '#XXX' is used for existing products, while (new) products launched since July 15, 2001 and RoHS compliant will use '-XXXE.'

# Package Outline Drawings

## 8-Pin DIP Package (6N139/6N138)<sup>2</sup>

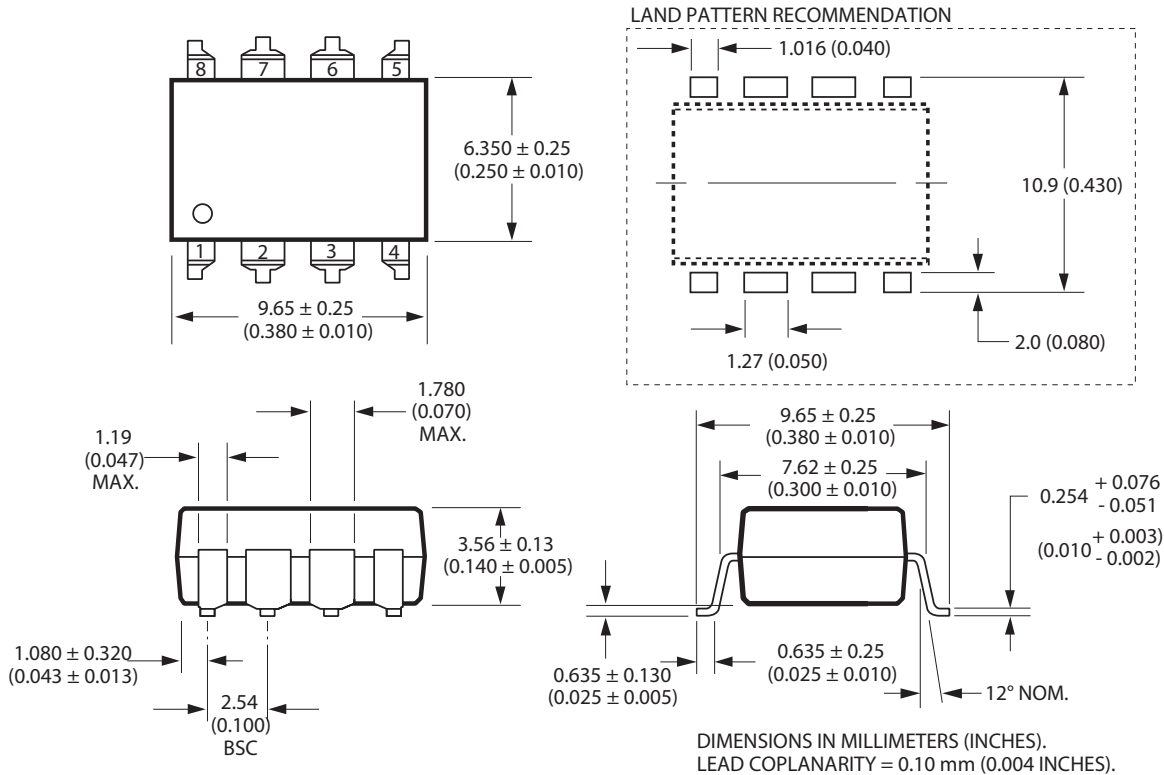


DIMENSIONS IN MILLIMETERS AND (INCHES).  
 \* MARKING CODE LETTER FOR OPTION NUMBERS  
 "L" = OPTION 020  
 OPTION NUMBERS 300 AND 500 NOT MARKED.

NOTE: FLOATING LEAD PROTRUSION IS 0.25 mm (10 mils) MAX.

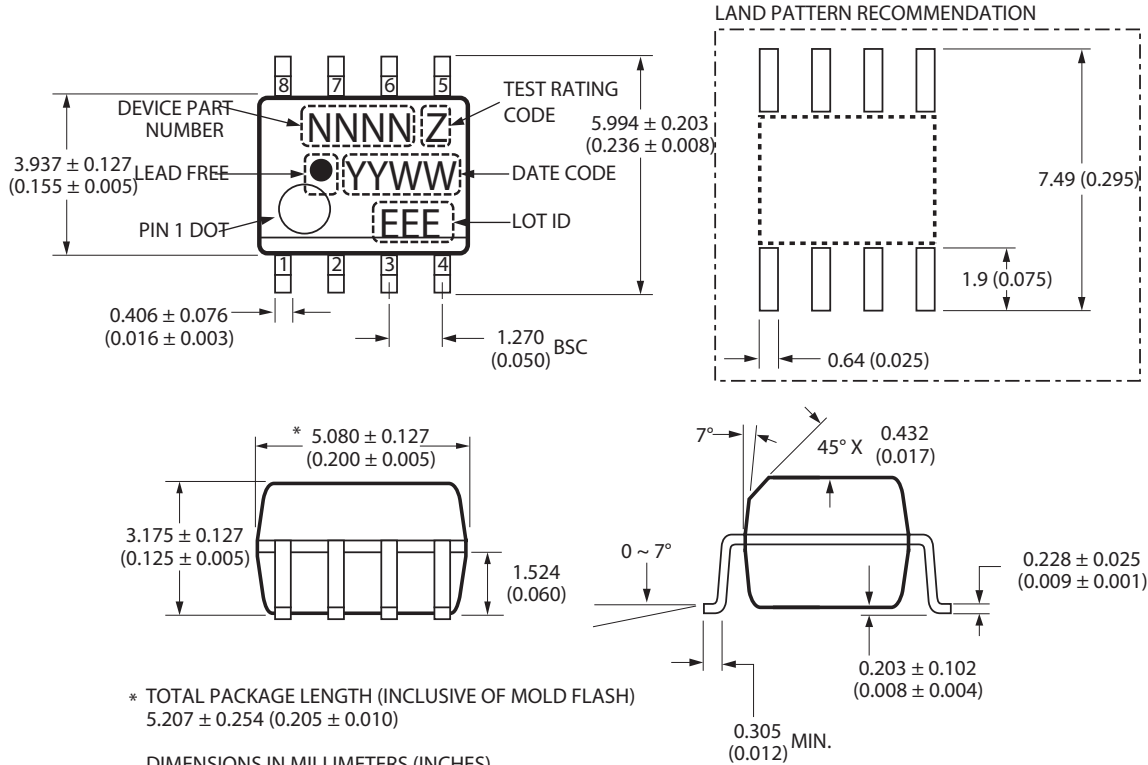
### 2. JEDEC Registered Data

## 8-Pin DIP Package with Gull Wing Surface Mount Option 300 (6N139/6N138)



NOTE: FLOATING LEAD PROTRUSION IS 0.25 mm (10 mils) MAX.

# Small Outline SO-8 Package (HCPL-0701/HCPL-0700)

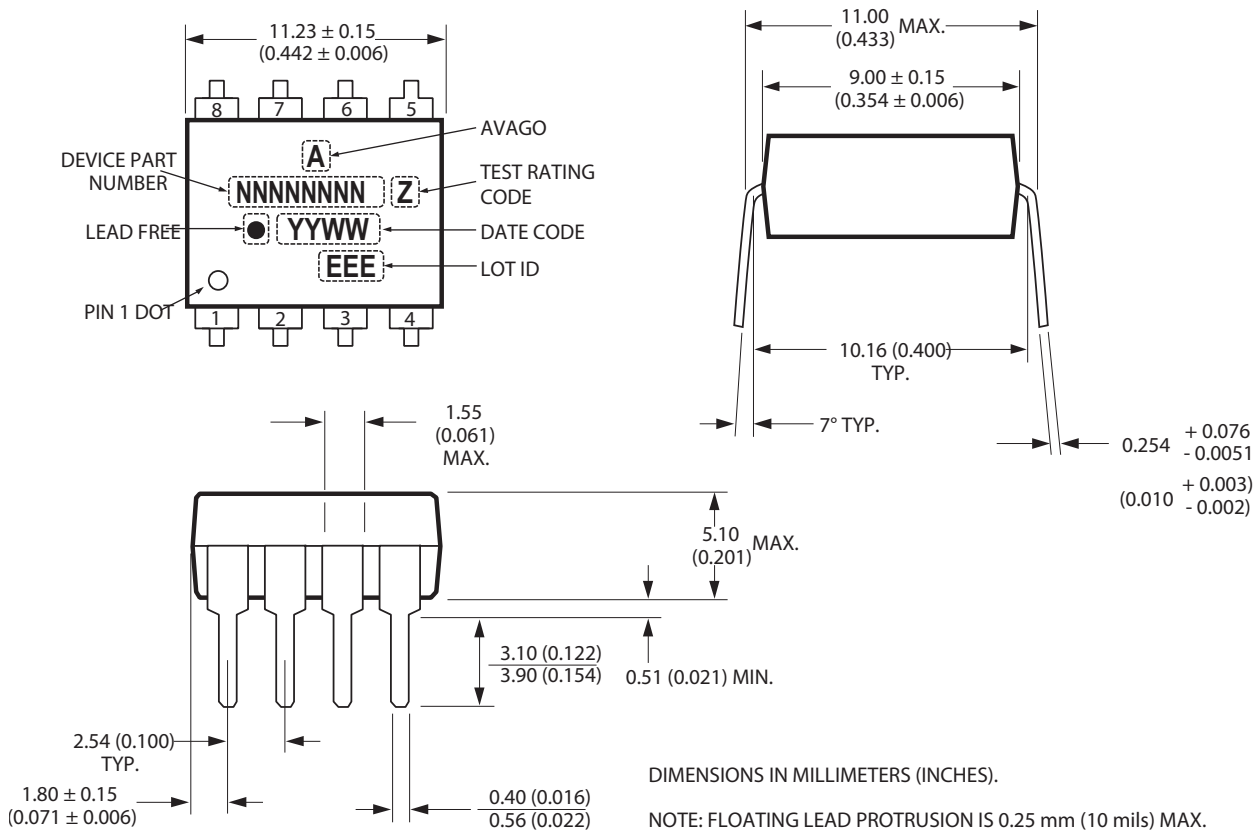


\* TOTAL PACKAGE LENGTH (INCLUSIVE OF MOLD FLASH)  
 $5.207 \pm 0.254$  ( $0.205 \pm 0.010$ )

DIMENSIONS IN MILLIMETERS (INCHES).  
 LEAD COPLANARITY =  $0.10$  mm ( $0.004$  INCHES) MAX.

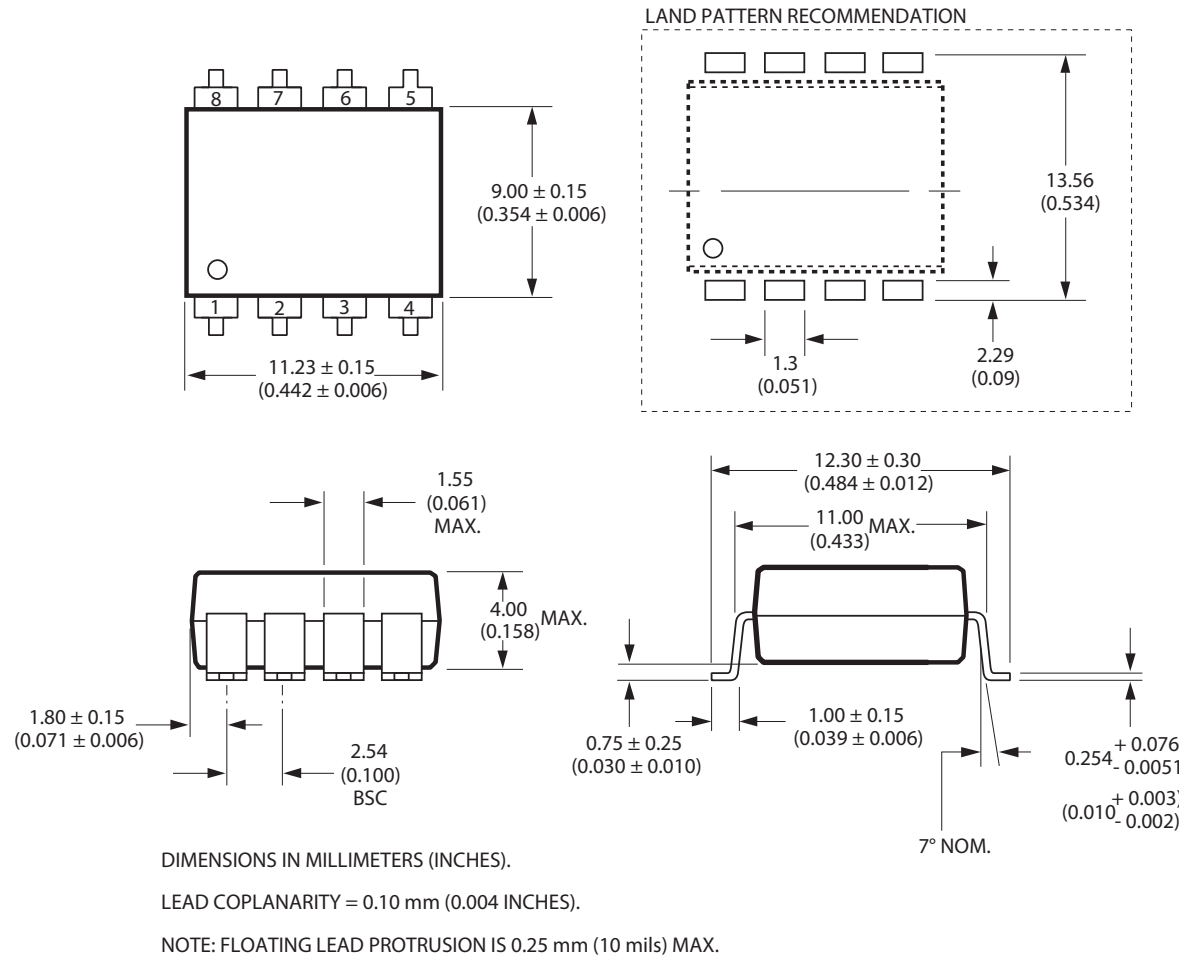
NOTE: FLOATING LEAD PROTRUSION IS  $0.15$  mm (6 mils) MAX.

## 8-Pin Widebody DIP Package (HCNW139/HCNW138)





# 8-Pin Widebody DIP Package with Gull Wing Surface Mount Option 300 (HCNW139/HCNW138)



## Solder Reflow Profile

The recommended reflow condition as per JEDEC Standard, J-STD-020 (latest revision). Use non-halide flux.

## Regulatory Information

The 6N139/138, HCNW139/138, and HCPL-0701/0700 have been approved by the following organizations.

<b>UL</b>	Recognized under UL 1577, Component Recognition Program, File E55361
<b>CSA</b>	Approved under CSA Component Acceptance Notice #5, File CA 88324.
<b>IEC/EN/DIN EN 60747-5-5</b>	HCNW139/138 and Option 060/360/560 only.

## Insulation and Safety Related Specifications

Parameter	Symbol	8-Pin DIP (300 Mil) Value	SO-8 Value	Widebody (400 Mil) Value	Units	Conditions
Minimum External Air Gap (External Clearance)	L(I01)	7.1	4.9	9.6	mm	Measured from input terminals to output terminals, shortest distance through air.
Minimum External Tracking (External Creepage)	L(I02)	7.4	4.8	10.0	mm	Measured from input terminals to output terminals, shortest distance path along body.
Minimum Internal Plastic Gap (Internal Clearance)		0.08	0.08	1.0	mm	Through insulation distance, conductor to conductor, usually the direct distance between the photoemitter and photodetector inside the optocoupler cavity.
Minimum Internal Tracking (Internal Creepage)		NA	NA	4.0	mm	Measured from input terminals to output terminals, along internal cavity.
Tracking Resistance (Comparative Tracking Index)	CTI	200	200	200	Volts	DIN IEC 112/VDE 0303 Part 1
Isolation Group		IIIa	IIIa	IIIa		Material Group (DIN VDE 0110, 1/89, Table 1)

Option 300 - surface-mount classification is Class A in accordance with CECC 00802.

## IEC/EN/DIN EN 60747-5-5 Insulation Characteristics (HCNW139 and HCNW138)

Description	Symbol	Characteristic	Units
Installation classification per DIN VDE 0110, Table 1			
for rated mains voltage $\leq 150$ Vrms		I – IV	
for rated mains voltage $\leq 300$ Vrms		I – IV	
for rated mains voltage $\leq 600$ Vrms		I – IV	
for rated mains voltage $\leq 1000$ Vrms		I – III	
Climatic Classification		0/70/21	
Pollution Degree (DIN VDE 0110/39)		2	
Maximum Working Insulation Voltage	$V_{IORM}$	1414	$V_{peak}$
Input to Output Test Voltage, Method b <sup>a</sup> $V_{IORM} \times 1.875 = V_{PR}$ , 100% Production Test with $t_m = 1s$ , Partial Discharge $< 5$ pC	$V_{PR}$	2651	$V_{peak}$
Input to Output Test Voltage, Method a <sup>a</sup> $V_{IORM} \times 1.6 = V_{PR}$ , Type and Sample Test, $t_m = 10s$ , Partial Discharge $< 5$ pC	$V_{PR}$	2262	$V_{peak}$
Highest Allowable Overvoltage (Transient Overvoltage, $t_{ini} = 60s$ )	$V_{IOTM}$	8000	$V_{peak}$
Safety Limiting Values (Maximum values allowed in the event of a failure)			
Case Temperature	$T_S$	150	$^{\circ}C$
Input Current	$I_{S, INPUT}$	400	mA
Output Power	$P_{S, OUTPUT}$	700	mW
Insulation Resistance at $T_S$ , $V_{IO} = 500V$	$R_S$	$>10^9$	$\Omega$

a. Refer to the optocoupler section of the current catalog, under Product Safety Regulations section, (IEC/EN/DIN EN 60747-5-5) for a detailed description.

**NOTE:** Isolation characteristics are guaranteed only within the safety maximum ratings which must be ensured by protective circuits in application.

## Absolute Maximum Ratings<sup>3</sup> (No Derating Required up to 85°C)

Parameter	Symbol	Min.	Max.	Units
Storage Temperature	$T_S$	-55	125	°C
Operating Temperature <sup>a</sup>	$T_A$	-40	85	°C
Average Forward Input Current	$I_{F(AVG)}$	—	20	mA
Peak Forward Input Current (50% duty cycle, 1 ms pulse width)	$I_{FPK}$	—	40	mA
Peak Transient Input Current (<1 μs pulse width, 300 pps)	$I_{F(TRAN)}$	—	1.0	A
Reverse Input Voltage	$V_R$	—	5	V
		HCNW139/138	3	V
Input Power Dissipation	$P_I$	—	35	mW
Output Current (pin 6)	$I_O$	—	60	mA
Emitter Base Reverse Voltage (pins 5 through 7)	$V_{EB}$	—	0.5	V
Supply Voltage and Output Voltage (6N139, HCPL-0701, HCNW139)	$V_{CC}$	-0.5	18	V
Supply Voltage and Output Voltage (6N138, HCPL-0700, HCNW138)	$V_{CC}$	-0.5	7	V
Output Power Dissipation	$P_O$	—	100	mW
Total Power Dissipation	$P_T$	—	135	mW
Lead Solder Temperature (for Through-Hole Devices)	HCNW139/138	260°C for 10s, 1.6 mm below seating plane		
		260°C for 10s, up to seating plane		
Reflow Temperature Profile (for SOIC-8 and Option #300)		See <a href="#">Package Outline Drawings</a> section		

a. 0°C to 70°C on JEDEC Registration.

## Recommended Operating Conditions

Parameter	Symbol	Min.	Max.	Units
Power Supply Voltage	$V_{CC}$	4.5	18	V
Forward Input Current (ON)	$I_{F(ON)}$	0.5	12.0	mA
Forward Input Voltage (OFF)	$V_{F(OFF)}$	0	0.8	V
Operating Temperature	$T_A$	0	70	°C

3. JEDEC Registered Data for 6N139 and 6N138.

# Electrical Specifications

$0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$ ,  $4.5\text{ V} \leq V_{CC} \leq 18\text{ V}$ ,  $0.5\text{ mA} \leq I_{F(\text{ON})} \leq 12\text{ mA}$ ,  $0\text{ V} \leq V_{F(\text{OFF})} \leq 0.8\text{ V}$ , unless otherwise specified.

All typicals at  $T_A = 25^{\circ}\text{C}$ . See Note 7.

Parameter	Symbol	Device	Min.	Typ. <sup>a</sup>	Max.	Units	Test Conditions	Fig.	Note			
Current Transfer Ratio	CTR	6N139 HCPL-0701	400 <sup>b</sup>	2000	5000	%	$I_F = 0.5\text{ mA}$ $V_{CC} = 4.5\text{ V}$ $V_O = 0.4\text{ V}$	2,3	1,2,4			
		HCNW139	400	4500	—							
		6N139 HCPL-0701	500 <sup>b</sup>	1600	2600		$I_F = 1.6\text{ mA}$					
		HCNW139	500	3000	—		$I_F = 5.0\text{ mA}$					
			300	1600	—							
			200	850	—					$I_F = 12\text{ mA}$		
		6N138 HCPL-0700	300 <sup>b</sup>	1600	2600		$I_F = 1.6\text{ mA}$					
HCNW138	1500	—										
Logic Low Output Voltage	$V_{OL}$	6N139 HCPL-0701 HCNW139	—	0.1	0.4	V	$V_{CC} = 4.5\text{ V}$	1	2			
				$I_F = 0.5\text{ mA}$ $I_O = 2\text{ mA}$						0.2		
											$I_F = 1.6\text{ mA}$ $I_O = 8\text{ mA}$	
												$I_F = 5.0\text{ mA}$ $I_O = 15\text{ mA}$
		6N138 HCPL-0700 HCNW138	0.1	$I_F = 1.6\text{ mA}$ $I_O = 4.8\text{ mA}$								
Logic High Output Current	$I_{OH}$	6N139 HCPL-0701 HCNW139	—	0.05	100	$\mu\text{A}$	$V_O = V_{CC} = 18\text{ V}$	$I_F = 0\text{ mA}$	2			
		6N138 HCPL-0700 HCNW138	—	0.1	250					$V_O = V_{CC} = 7\text{ V}$		
Logic Low Supply Current	$I_{CCL}$	6N138/139 HCPL-0701/ 0700	—	0.4	1.5	mA	$I_F = 1.6\text{ mA}$ , $V_O = \text{Open}$ , $V_{CC} = 18\text{ V}$	10	2			
		HCNW139	—	0.5	2							
		HCNW138	—	—	—							
Logic High Supply Current	$I_{CCH}$	6N138/139 HCPL-0701/ 0700	—	0.01	10	$\mu\text{A}$	$I_F = 0\text{ mA}$ , $V_O = \text{Open}$ , $V_{CC} = 18\text{ V}$		2			
		HCNW139 HCNW138	—	—	1							

Parameter	Symbol	Device	Min.	Typ. <sup>a</sup>	Max.	Units	Test Conditions	Fig.	Note
Input Forward Voltage	$V_F$	6N138	1.25	1.40	1.7 <sup>b</sup>	V	$T_A = 25^\circ\text{C}$ $I_F = 1.6\text{ mA}$	4,8	
		6N139	—		1.75				
		HCPL-0701							
		HCPL-0700							
		HCNW139	1.0	1.45	1.85		$T_A = 25^\circ\text{C}$		
HCNW138	0.95	—	1.95						
Input Reverse Breakdown Voltage	$BV_R$		5.0 <sup>b</sup>	—	—	V	$I_R = 10\ \mu\text{A}, T_A = 25^\circ\text{C}$		
		HCNW139	3.0				$I_R = 100\ \mu\text{A}, T_A = 25^\circ\text{C}$		
HCNW138									
Temperature Coefficient of Forward Voltage	$\Delta V_F/\Delta T_A$		—	-1.8	—	mV/°C	$I_F = 1.6\text{ mA}$	8	
Input Capacitance	$C_{IN}$		—	60	—	pF	$f = 1\text{ MHz}, V_F = 0\text{V}$		
		HCNW139		90					
HCNW138									

a. All typical values are at  $T_A = 25^\circ\text{C}$ ,  $V_{CC} = 5\text{V}$ , unless otherwise noted.

b. JEDEC Registered Data for 6N139 and 6N138.

# Switching Specifications

Over recommended conditions ( $T_A = 0^\circ\text{C}$  to  $70^\circ\text{C}$ ),  $V_{CC} = 5.0\text{V}$ , unless otherwise specified.

Parameter	Symbol	Device	Min.	Typ. <sup>a</sup>	Max.		Units	Test Conditions	Fig.	Note
					$T_A = 25^\circ\text{C}$					
Propagation Delay Time to Logic Low at Output	$t_{PHL}$	6N139 HCPL-0701 HCNW139	—	5	25 <sup>b</sup>	30	$\mu\text{s}$	$I_F = 0.5\text{ mA}$ , $R_I = 4.7\text{ k}\Omega$	5,6,7,9	2,4
		6N139 HCPL-0701 HCNW139	—	0.2	1 <sup>b</sup>	2	$\mu\text{s}$	$I_F = 12\text{ mA}$ , $R_I = 270\Omega$		
			11							
		6N138 HCPL-0700 HCNW138	—	1.6	10 <sup>b</sup>	15	$\mu\text{s}$	$I_F = 1.6\text{ mA}$ , $R_I = 2.2\text{ k}\Omega$		
			11							
Propagation Delay Time to Logic High at Output	$t_{PLH}$	6N139 HCPL-0701 HCNW139	—	18	60 <sup>b</sup>	90	$\mu\text{s}$	$I_F = 0.5\text{ mA}$ , $R_I = 4.7\text{ k}\Omega$	5,6,7,9	2,4
		6N139 HCPL-0701 HCNW139	—	2	7 <sup>b</sup>	10	$\mu\text{s}$	$I_F = 12\text{ mA}$ , $R_I = 270\Omega$		
			11							
		6N138 HCPL-0700 HCNW138	—	10	35 <sup>b</sup>	50	$\mu\text{s}$	$I_F = 1.6\text{ mA}$ , $R_I = 2.2\text{ k}\Omega$		
			70							
		Common Mode Transient Immunity at Logic High Output	$ CM_H $		1000	10000	—	—		
Common Mode Transient Immunity at Logic Low Output	$ CM_L $		1000	10000	—	—	$\text{V}/\mu\text{s}$	$I_F = 1.6\text{ mA}$ , $T_A = 25^\circ\text{C}$ , $R_I = 2.2\text{ k}\Omega$ , $ V_{CM}  = 10\text{Vp-p}$	13	5,6

a. All typical values at  $T_A = 25^\circ\text{C}$ ,  $V_{CC} = 5\text{V}$ , unless otherwise noted.

b. JEDEC Registered Data for 6N139 and 6N138.

## Package Characteristics

Parameter	Symbol	Min.	Typ. <sup>a</sup>	Max.	Units	Test Conditions	Fig.	Note
Input-Output Momentary Withstand Voltage <sup>b</sup>	V <sub>ISO</sub>	3750	—	—	V rms	RH < 50%, t = 1 minute, T <sub>A</sub> = 25°C		3,8
6N138/6N139 Option x20 HCNW139 HCNW138		5000	—	—				3,9
Resistance (Input-Output)	R <sub>I-O</sub>	—	10 <sup>12</sup>	—	Ω	V <sub>I-O</sub> = 500 Vdc RH < 45%		3
Capacitance (Input-Output)	C <sub>I-O</sub>	—	0.6	—	pF	f = 1 MHz		3

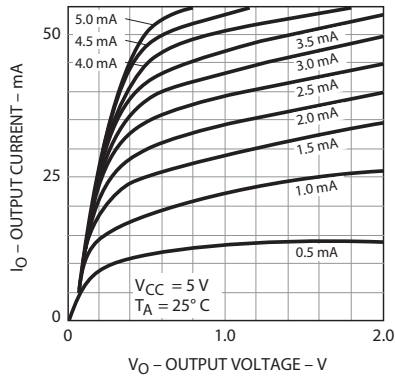
a. All typicals at T<sub>A</sub> = 25°C, unless otherwise noted.

b. The Input-Output Momentary Withstand Voltage is a dielectric voltage rating that should not be interpreted as an input-output continuous voltage rating.

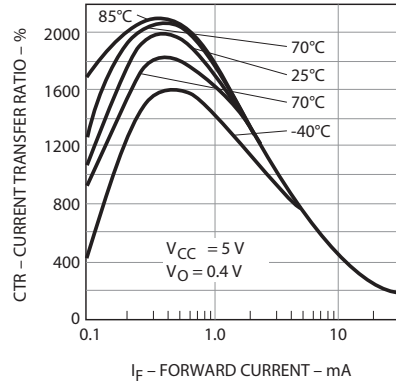
### NOTE:

- DC CURRENT TRANSFER RATIO (CTR) is defined as the ratio of output collector current, I<sub>O</sub>, to the forward LED input current, I<sub>F</sub>, times 100%.
- Pin 7 Open.
- Device considered a two-terminal device: Pins 1, 2, 3, and 4 shorted together and pins 5, 6, 7, and 8 shorted together.
- Use of a resistor between pin 5 and 7 will decrease gain and delay time. Significant reduction in overall gain can occur when using resistor values below 47 kΩ.
- Common mode transient immunity in a Logic High level is the maximum tolerable (positive) dV<sub>CM</sub>/dt of the common mode pulse, V<sub>CM</sub>, to assure that the output will remain in a Logic High state (i.e., V<sub>O</sub> > 2.0 V). Common mode transient immunity in a Logic Low level is the maximum tolerable (negative) dV<sub>CM</sub>/dt of the common mode pulse, V<sub>CM</sub>, to assure that the output will remain in a Logic Low state (i.e., V<sub>O</sub> < 0.8 V).
- In applications where dV/dt may exceed 50,000 V/μs (such as static discharge) a series resistor, R<sub>CC</sub>, should be included to protect the detector IC from destructively high surge currents. The recommended value is R<sub>CC</sub> = 220Ω.
- Use of a 0.1-μF bypass capacitor connected between pins 8 and 5 adjacent to the device is recommended.
- In accordance with UL 1577, each optocoupler is proof tested by applying an insulation test voltage 4500 V rms for 1 second (leakage detection current limit, I<sub>I-O</sub> < 5 μA). This test is performed before the 100% production test shown in the IEC/EN/DIN EN 60747-5-5 Insulation Related Characteristics Table, if applicable.
- In accordance with UL 1577, each optocoupler is proof tested by applying an insulation test voltage > 6000 V rms for 1 second (leakage detection current limit, I<sub>I-O</sub> < 5 μA). This test is performed before the 100% production test for partial discharge (method b) shown in the IEC/EN/DIN EN 60747-5-5 Insulation Related Characteristics Table, if applicable.

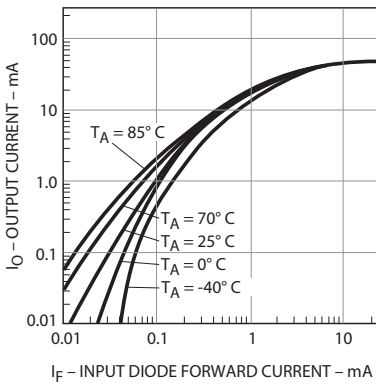
**Figure 1: 6N138/6N139 DC Transfer Characteristics**



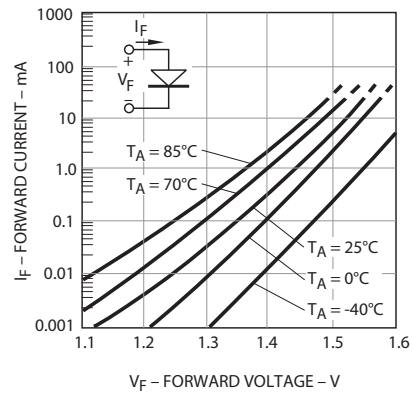
**Figure 2: Current Transfer Ratio vs. Forward Current 6N138/6N139**



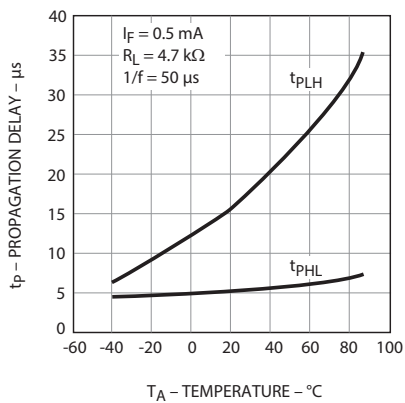
**Figure 3: 6N138/6N139 Output Current vs. Input Diode Forward Current**



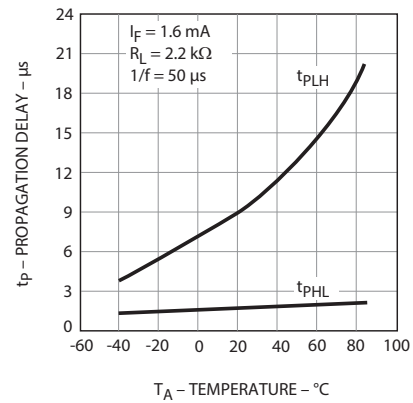
**Figure 4: Input Diode Forward Current vs. Forward Voltage**



**Figure 5: Propagation Delay vs. Temperature**

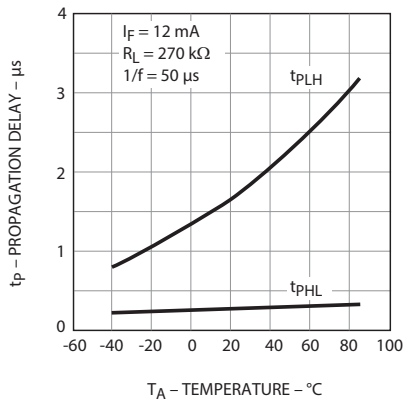


**Figure 6: Propagation Delay vs. Temperature**

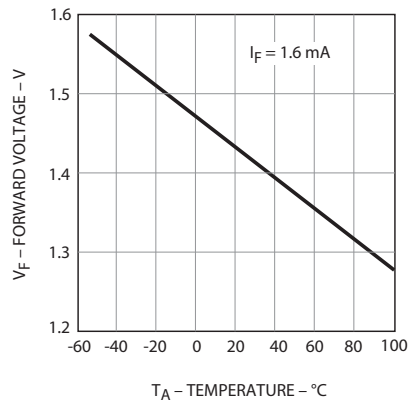




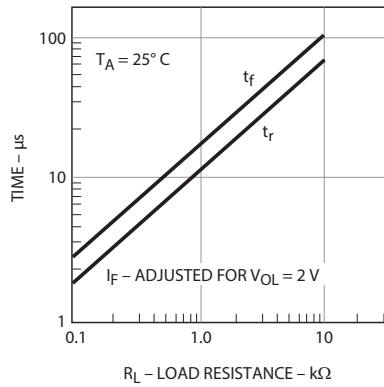
**Figure 7: Propagation Delay vs. Temperature**



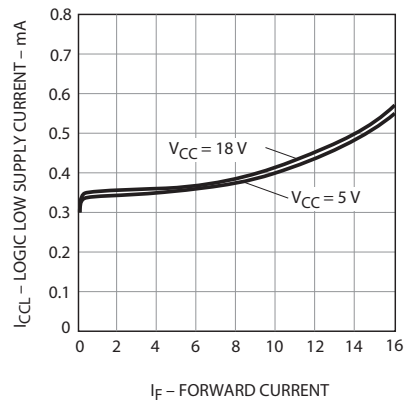
**Figure 8: Forward Voltage vs. Temperature**



**Figure 9: Nonsaturated Rise and Fall Times vs. Load Resistance**



**Figure 10: Logic Low Supply Current vs. Forward Current**



**Figure 11: Thermal Derating Curve, Dependence of Safety-Limiting Value with Case Temperature per IEC/EN/DIN EN60747-5-5**

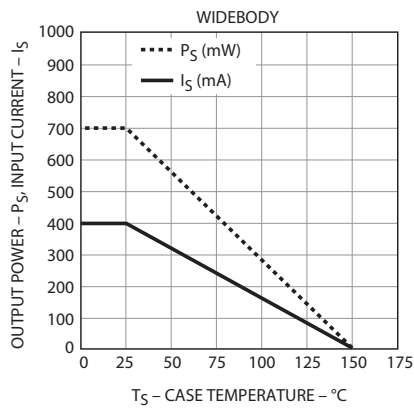


Figure 12: Switching Test Circuit

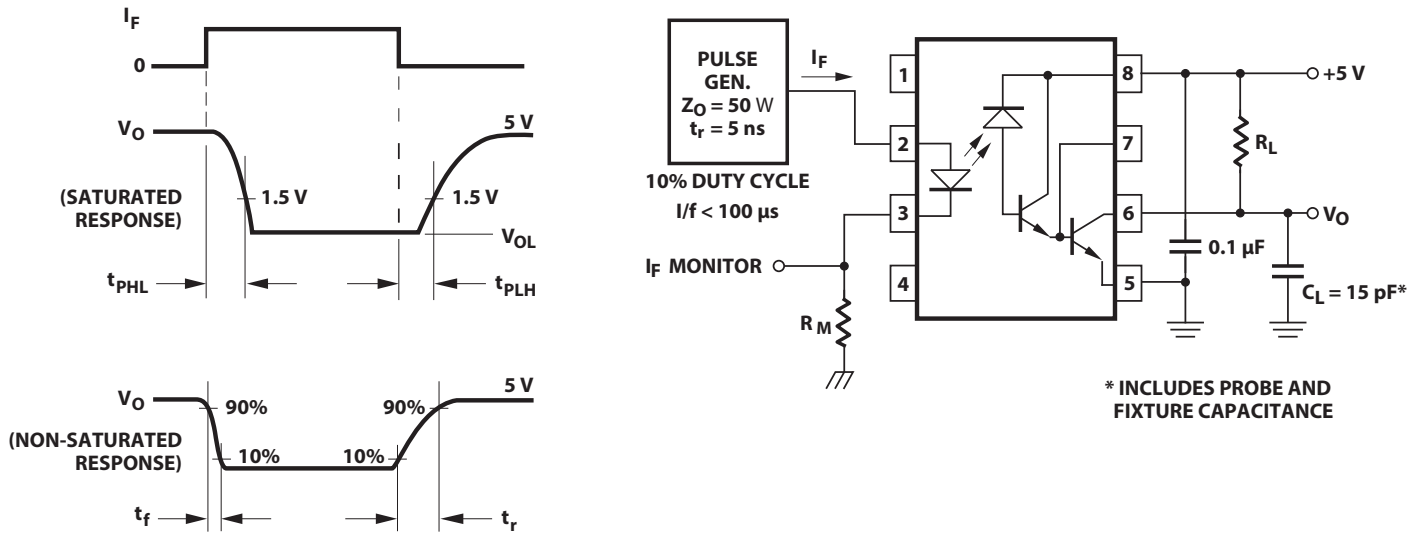
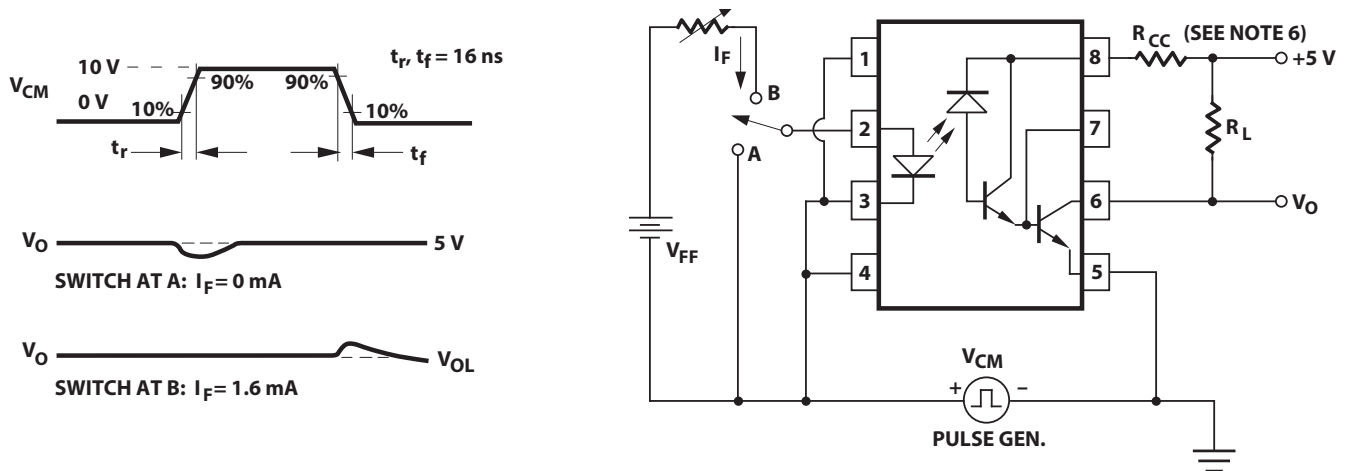


Figure 13: Test Circuit for Transient Immunity and Typical Waveforms



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