

# BCM95347R24WS, BCM95347R24M





## SINGLE-CHIP L2+ MANAGED SWITCH WITH 24 10/100 PORTS + FOUR GBE PORTS

#### **FEATURES**

- The BCM95347R24WS and BCM95347R24M is a complete 24 FE port + 4 GE port reference design and evaluation system. The reference design is based on the BCM5347 device, a powerful, highly-integrated product of the ROBOswitch<sup>TM</sup> product family.
- The reference design provides system developers with a solid platform for product evaluation and software development.
- The BCM95347R24WS and BCM95347R24M reference platform also includes a 32-bit MIPS CPU subsystem capable of running advanced management software.
- Complete design documentation, including board schematic and layout, and Broadcom API source code is available.

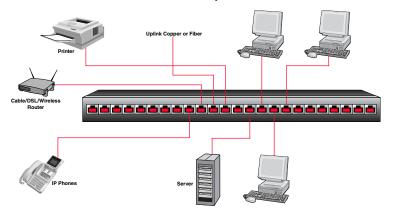
The reference design includes the following:

- Main PCB with BCM5347 switch
- CPU subsyem with BCM5836 CPU
- Latest version of the Broadcom API driver
- Support for Linux or ECOS
- Flexible Web-based management software

#### SUMMARY OF BENEFITS

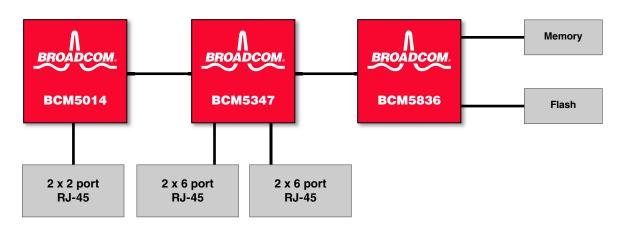
- The BCM95347R24WS and BCM95347R24M includes:
  - Twenty-four 10/100 transceivers for TX/FX
  - Forty-eight media access controllers
  - 5-Gb media access controllers
  - 4-Mb total of packet buffer and control memory
  - Nonblocking switch fabric supports up to 24 FE + 4 GbE ports
- Flexible TCAM-based ContentAware<sup>TM</sup> Classification technology for affordable security and Access Control List
- Double Tagging enables ISP applications.
- Enables a new generation of lower-cost FE switches with the highest integration of enhanced L2+ features and maximum connectivity.
- Optimized for WebSuperSmart<sup>TM</sup> managed switch design.
- 802.1Q VLAN allows segmentation of network into smaller broadcast domains to preserve bandwidth and increase network security.
- Four flexible Class of Service (CoS) queues per port ensure the lowest latency to high-priority traffic. This enables the switch to support a wide variety of delay-sensitive video and audio multicast applications.
- IEEE 802.3ad link aggregation increases effective bandwidth and provides redundancy of a link.
- IEEE 802.1X Extensive Authentication Protocol over LAN (EAPOL) plus MAC address locking enhances network port security.
- Storm and Bandwidth/Rate Control provide better traffic profile management.
- Access to all internal registers through EB Bus, SPI, or MDC/ MDIO interfaces.
- Hardware support for implementing SNMP and RMON.
- Low-power consumption and embedded regulators allow simple PCB design.

#### 24-Port FE Switch for Small/Medium Businesses





## OVERVIEW



### **Functional Block Diagram**

#### **Documentation and Software**

Documentation for the BCM95347R24WS and BCM95347R24M platforms, SDK, software API, and reference design is available online through docSAFE:

- Complete BCM reference design files, including BCM95347R24WS and BCM95347R24M schematics, Gerber layout, and BOM.
- Reference Design Quick Start Guide
- SDK software API source code and documentation

### **Ordering Information**

The ordering part numbers are:

- BCM95347R24WS for 24+4 WebSuperSmart Platform
- BCM95347R24M for 24+4 Managed Platform

For additional ordering information on the BCM95347R24WS and BCM95347R24M reference design and evaluation system, contact your local sales representative.

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