AMMP-6222
7 to 21 GHz GaAs High Linearity LNA in SMT Package

Data Sheet

Description
Avago Technologies’ AMMP-6222 is an easy-to-use broadband, high gain, high linearity Low Noise Amplifier in a surface mount package. The wide band and unconditionally stable performance makes this MMIC ideal as a primary or sub-sequential low noise block or a transmitter or LO driver. The MMIC has 3 gain stages and a selectable pin to switch between low and high current, corresponding with low and high output power and linearity. In the high current, high output power state, it requires a 4V, 120mA supply. In the low current, low output power state, the supply is reduced to 4V, 95mA. Since this MMIC covers several bands, it can reduce part inventory and increase volume purchase options. The MMIC is fabricated using PHEMT technology. The surface mount package eliminates the need of “chip & wire” assembly for lower cost. This MMIC is fully SMT compatible with backside grounding and I/Os.

Features
- Surface Mount Package, 5.0 x 5.0 x 1.25 mm
- Single Positive Bias Pin
- Selectable Output Power / Linearity
- No Negative Gate Bias

Specifications (Vdd = 4.0V, Idd = 120mA)
- RF Frequencies: 7 - 21 GHz
- High Output IP3: 29dBm
- High Small-Signal Gain: 24dB
- Typical Noise Figure: 2.3dB
- Input, Output Match: -10dB

Applications
- Microwave Radio systems
- Satellite VSAT, DBS Up/Down Link
- LMDS & Pt-Pt mmW Long Haul
- Broadband Wireless Access (including 802.16 and 802.20 WiMax)
- WLL and MMDS loops

Functional Block Diagram

Note: MSL Rating = Level 2A
Electrical Specifications
1. Small/Large -signal data measured in a fully de-embedded test fixture form TA = 25°C.
2. Pre-assembly into package performance verified 100% on-wafer per AMMC-6222 published specifications.
3. This final package part performance is verified by a functional test correlated to actual performance at one or more frequencies.
4. Specifications are derived from measurements in a 50 Ω test environment. Aspects of the amplifier performance may be improved over a more narrow bandwidth by application of additional conjugate, linearity, or low noise (Гopt) matching.
5. All tested parameters guaranteed with measurement accuracy +/-0.5dB for gain and +/-0.3dB for NF in the high output power configuration.

Table 1. RF Electrical Characteristics
TA=25°C, Id=120mA, Vd=4.0V, Zo=50 Ω

<table>
<thead>
<tr>
<th>Parameter</th>
<th>High Output Power Configuration</th>
<th>Lower Output Power Configuration</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Min</td>
<td>Typical</td>
</tr>
<tr>
<td>Drain Current, Id</td>
<td>120</td>
<td></td>
</tr>
<tr>
<td>Small Signal Gain, Gain</td>
<td>19</td>
<td>24</td>
</tr>
<tr>
<td>Noise Figure into 50 Ω, NF</td>
<td>2.3</td>
<td>3.5</td>
</tr>
<tr>
<td>Output Power at 1dB Gain Compression, P1dB</td>
<td>15.5</td>
<td></td>
</tr>
<tr>
<td>Output Power at 3dB Gain Compression, P3dB</td>
<td>17.5</td>
<td></td>
</tr>
<tr>
<td>Output Third Order Intercept Point, OIP3</td>
<td>29</td>
<td></td>
</tr>
<tr>
<td>Isolation, Iso</td>
<td>-45</td>
<td></td>
</tr>
<tr>
<td>Input Return Loss, Rlin</td>
<td>-10</td>
<td></td>
</tr>
<tr>
<td>Output Return Loss, RLout</td>
<td>-10</td>
<td></td>
</tr>
</tbody>
</table>

Table 2. Recommended Operating Range
1. Ambient operational temperature TA = 25°C unless otherwise noted.
2. Channel-to-backside Thermal Resistance (Tchannel (Tc) = 34°C) as measured using infrared microscopy. Thermal Resistance at backside temperature (Tb) = 25°C calculated from measured data.

<table>
<thead>
<tr>
<th>Description</th>
<th>Min.</th>
<th>Typical</th>
<th>Max.</th>
<th>Unit</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>Drain Supply Current, Id</td>
<td>80</td>
<td>120</td>
<td>160</td>
<td>mA</td>
<td>Vd = 4.5 V, Under any RF power drive and temperature</td>
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<tr>
<td>Drain Supply Voltage, Vd</td>
<td>3</td>
<td>4</td>
<td>5</td>
<td>V</td>
<td></td>
</tr>
</tbody>
</table>
Table 3. Thermal Properties

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermal Resistance, $\theta_{jc}$</td>
<td>Ambient operational temperature $T_A = 25^\circ C$</td>
<td>$\theta_{jc} = 31.47 \degree C/W$</td>
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<tr>
<td></td>
<td>Channel-to-backside Thermal Resistance $T_{channel}(T_c)=34^\circ C$</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Thermal Resistance at backside temperature $T_b=25^\circ C$</td>
<td></td>
</tr>
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</table>

Absolute Minimum and Maximum Ratings

Table 4. Minimum and Maximum Ratings

<table>
<thead>
<tr>
<th>Description</th>
<th>Min.</th>
<th>Max.</th>
<th>Unit</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>Drain to Ground Supply Voltage, $V_d$</td>
<td>5.5</td>
<td>V</td>
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<td></td>
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<tr>
<td>Drain Current, $I_d$</td>
<td>170</td>
<td>mA</td>
<td></td>
<td></td>
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<tr>
<td>RF CW Input Power, $P_{in}$</td>
<td>10</td>
<td>dBm</td>
<td>CW</td>
<td></td>
</tr>
<tr>
<td>Channel Temperature, $T_{ch}$</td>
<td>+150</td>
<td>°C</td>
<td></td>
<td></td>
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<tr>
<td>Storage Temperature, $T_{stg}$</td>
<td>-65</td>
<td>+150</td>
<td>°C</td>
<td>20 second maximum</td>
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<tr>
<td>Maximum Assembly Temperature, $T_{max}$</td>
<td>260</td>
<td>°C</td>
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<td></td>
</tr>
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</table>

Notes: 1. Operation in excess of any one of these conditions may result in permanent damage to this device.
AMMP-6222 Typical Performance for High Current, High Output Power Configuration[1],[2]

($T_A = 25^\circ C$, $V_{dd}=4V$, $I_{dd}=120mA$, $Z_{in} = Z_{out} = 50 \, \Omega$ unless noted)

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**Figure 1a. Small-signal Gain**

**Figure 2a. Noise Figure**

**Figure 3a. Input Return Loss**

**Figure 4a. Output P-1dB**

**Figure 5a. Output Return Loss**

**Figure 6a. Output IP3**

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**Note:**

1. S-parameters are measured with R&D Eval Board as shown in Figure 21. Board and connector effects are included in the data.
2. Noise Figure is measured with R&D Eval board as shown in Figure 21, and with a 3-dB pad at input. Board and connector losses are already de-embedded from the data.
AMMP-6222 Typical Performance for High Current, High Output Power Configuration (Cont)

(T_A = 25°C, Vdd=4V, Idd=120mA, Zin = Zout = 50 Ω unless noted)

Figure 7a. Isolation

Figure 8a. Idd over Vdd

Figure 9a. Small-signal Gain Over Vdd

Figure 10a. Noise Figure Over Vdd

Figure 11a. Input Return Loss Over Vdd

Figure 12a. Output Return Loss Over Vdd
AMMP-6222 Typical Performance for High Current, High Output Power Configuration (Cont)

(TA = 25°C, Vdd=4V, Idd=120mA, Zin = Zout = 50 Ω unless noted)

Figure 13a. Output P1dB over Vdd

Figure 14a. Output IP3 over Vdd

Figure 15a. Small-signal Gain Over Temp

Figure 16a. Noise Figure Over Temp

Figure 17a. Input Return Loss Over Temp

Figure 18a. Output Return Loss Over Temp
AMMP-6222 Typical Performance for Low Current, Low Output Power Configuration \[1], \[2]
\(T_A = 25^\circ\text{C}, V_{dd}=4\text{V}, I_{dd}=95\text{mA}, Z_{in} = Z_{out} = 50\ \Omega\) unless noted

![Small-signal Gain](Figure 1b)

![Noise Figure](Figure 2b)

![Input Return Loss](Figure 3b)

![Output P-1dB](Figure 4b)

![Output Return Loss](Figure 5b)

![Output IP3](Figure 6b)

Note:
1. S-parameters are measured with R&D Eval Board as shown in Figure 21. Board and connector effects are included in the data.
2. Noise Figure is measured with R&D Eval board as shown in Figure 21, and with a 3-dB pad at input. Board and connector losses are already de-embedded from the data
AMMP-6222 Typical Performance for Low Current, Low Output Power Configuration (Cont)

\( T_A = 25^\circ C, V_{dd}=4V, I_{dd}=95mA, Z_{in} = Z_{out} = 50 \, \Omega \) unless noted

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**Figure 7b. Isolation**

**Figure 8b. \( I_{dd} \) over \( V_{dd} \)**

**Figure 9b. Small-signal Gain Over \( V_{dd} \)**

**Figure 10b. Noise Figure Over \( V_{dd} \)**

**Figure 11b. Input Return Loss Over \( V_{dd} \)**

**Figure 12b. Output Return Loss Over \( V_{dd} \)**
AMMP-6222 Typical Performance for Low Current, Low Output Power Configuration (Cont)

\( T_A = 25^\circ C, V_{dd}=4V, I_{dd}=95mA, Z_{in} = Z_{out} = 50 \ \Omega \) unless noted

![Figure 13b. Output P1dB over Vdd](image1)

![Figure 14b. Output IP3 over Vdd](image2)

![Figure 15b. Small-signal Gain Over Temp](image3)

![Figure 16b. Noise Figure Over Temp](image4)

![Figure 17b. Input Return Loss Over Temp](image5)

![Figure 18b. Output Return Loss Over Temp](image6)
AMMP-6222 Application and Usage

Biasing and Operation

The AMMP-6222 is normally biased with a positive drain supply connected to the VDD pin through bypass capacitor as shown in Figures 19 and 20. The recommended drain supply voltage for general usage is 4V and the corresponding drain current is approximately 120mA. It is important to have 0.1uF bypass capacitor and the capacitor should be placed as close to the component as possible. Aspects of the amplifier performance may be improved over a narrower bandwidth by application of additional conjugate, linearity, or low noise (Topt) matching.

For receiver front end low noise applications where high power and linearity are not often required, the AMMP-6222 can be set in low current state when pin # 5 is open as shown in Figure 19. In this configuration, the bias current is approximately 90mA, 95mA and 100mA for 3V, 4V and 5V respectively.

In applications where high output power and linearity are often required such as LO or transmitter drivers, the AMMP-6222 can be selected to operate at its highest output power by grounding pin # 5 as shown in Figure 20. At 5V, the amplifier can provide Psat of ~ 20dBm. The bias current in this configuration is 115mA, 120mA and 125mA for 3V, 4V and 5V respectively.

Refer the Absolute Maximum Ratings table for allowed DC and thermal conditions.

Figure 19. Low Current, Low Output Power State

Figure 20. High Current, High Output Power State

Figure 21. Evaluation/Test Board (available to qualified customer request)

Figure 22. Simplified High Linearity LNA Schematic
Typical Scattering Parameters

Please refer to <http://www.avagotech.com> for typical scattering parameters data.

Package Dimension, PCB Layout and Tape and Reel information

Please refer to Avago Technologies Application Note 5520, AMXP-xxxx production Assembly Process (Land Pattern A).

Ordering Information

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<thead>
<tr>
<th>Part Number</th>
<th>Devices Per Container</th>
<th>Container</th>
</tr>
</thead>
<tbody>
<tr>
<td>AMMP-6222-BLKG</td>
<td>10</td>
<td>Antistatic bag</td>
</tr>
<tr>
<td>AMMP-6222-TR1G</td>
<td>100</td>
<td>7” Reel</td>
</tr>
<tr>
<td>AMMP-6222-TR2G</td>
<td>500</td>
<td>7” Reel</td>
</tr>
</tbody>
</table>